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OFFICIAL**To:** Examiner F. Erdem
Group Art Unit: 2826**From:** Leslie S. Szlvos, Ph.D.**Fax:** 703-872-9306**Pages:** 7 including cover**Phone:****Date:** March 8, 2004**Re:** Victor W. C. Chan, et al.
U.S. Patent Appln. No. 10/604,003
Our Docket: FIS920030105US1 (16462)**CC:****XX Urgent** ☐ **For Review** ☐ **Please Comment** ☐ **Please Reply** ☐ **Please Recycle**

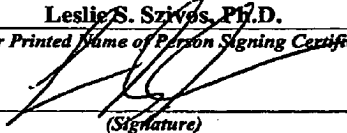
Dear Sir:

The following is attached hereto:


1. Amendment Transmittal in duplicate
2. Response under 37 CFR 1.111 and 1.143
3. Certificate of Facsimile

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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)			Docket No.
Applicant(s): Victor W.C. Chan, et al.			FIS920030105US1 (16462)
Serial No. 10/604,003	Filing Date 6/20/2003	Examiner F. Erdem	Group Art Unit 2826
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE			
<p>I hereby certify that this <u>RESPONSE UNDER 1.111 AND 1.143</u> (Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u>)</p> <p>on <u>March 8, 2004</u> (Date)</p> <p style="text-align: center;"><u>Leslie S. Szivas, Ph.D.</u> (Typed or Printed Name of Person Signing Certificate)</p> <p style="text-align: center;"> (Signature)</p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p>			

P18/REV01

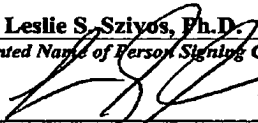
AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No.	
Applicant(s): Victor W.C. Chan, et al.				FIS920030105US1 (16462)	
Serial No. 10/604,003	Filing Date 6/20/2003	Examiner F. Erdem	Group Art Unit 2826		
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE					
<u>TO THE COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	17 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	2 -	3 =	0 x	\$84.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
<div><input checked="" type="checkbox"/> No additional fee is required for amendment.</div> <div><input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____</div> <div><input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed.</div> <div><input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 09-0458/IBM<div><input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16.</div><div><input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.</div></div>					
 _____ <i>Signature</i>			Dated: March 8, 2004		
Leslie S. Szivos, Ph.D. Registration No. 39,394 Customer No. 23389					
<div><div>cc:</div><div style="border: 1px solid black; padding: 5px;"><div>I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div><div>_____ <i>Signature of Person Mailing Correspondence</i></div><div>_____ <i>Typed or Printed Name of Person Mailing Correspondence</i></div></div></div>					

P11LARGE/REV06

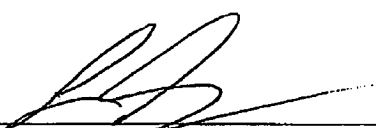
Serial No. 10/604,003		Filing Date 6/20/2003	Examiner F. Erdem	Docket No. FIS920030105US1 (16462)
Group Art Unit 2826				

Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE

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is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. 703-872-9306)
on March 8, 2004
(Date)

Leslie S. Szivos, Ph.D.
(Typed or Printed Name of Person Signing Certificate)

(Signature)

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AMENDMENT TRANSMITTAL LETTER (Large Entity)				Docket No.	
Applicant(s): Victor W.C. Chan, et al.				FIS920030105US1 (16462)	
Serial No. 10/604,003	Filing Date 6/20/2003	Examiner F. Erdem	Group Art Unit 2826		
Invention: SUBSTRATE ENGINEERING FOR OPTIMUM CMOS DEVICE PERFORMANCE					
<u>TO THE COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.					
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TOTAL CLAIMS	17 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	2 -	3 =	0 x	\$84.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00
 <input checked="" type="checkbox"/> No additional fee is required for amendment. <input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____ <input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed. <input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 09-0458/IBM <input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16. <input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.					
 _____ <i>Signature</i>			Dated: March 8, 2004		
Leslie S. Szivos, Ph.D. Registration No. 39,394 Customer No. 23389					
cc:			<div style="border: 1px solid black; padding: 5px;"><div style="text-align: center;">I certify that this document and fee is being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div><div style="text-align: center;">_____ Signature of Person Mailing Correspondence</div><div style="text-align: center;">_____ Typed or Printed Name of Person Mailing Correspondence</div></div>		

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MAR 08 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Applicant(s):** Victor W.C. Chan, et al.**Examiner:** F. Erdem**Serial No:** 10/604,003**Art Unit:** 2826**Filed:** June 20, 2003**Docket:** FIS920030105US1 (16462)**For:** SUBSTRATE ENGINEERING FOR
OPTIMUM CMOS DEVICE PERFORMANCE**Dated:** March 8, 2004Commissioner for Patents
United States Patent Office
Alexandria, VA 23313-1450**RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143**

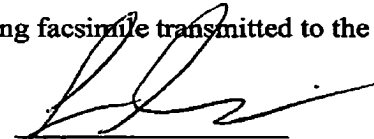
Sir:

In response to the Office Action dated February 19, 2004, applicants provisionally elect, without traverse, the subject matter of Group II, i.e., Claims 11-17, for continued prosecution in the above-identified patent application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: March 8, 2004


Leslie S. Szivos, Ph.D.

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REMARKS

Favorable consideration and allowance of the claims of the present application are respectfully requested.

In the present Official Action, Claims 1-17 which are present in the above-captioned application have been subjected to a restriction under 35 U.S.C. §121 as follows:

- I. Claims 1-10, drawn to a method of making a semiconductor device, classified in Class 438, subclass 198; and
- II. Claims 11-17, drawn to a semiconductor device, classified in Class 257, subclass 627.

As indicated, and in order to be fully responsive to the restriction requirement imposed in the present Official Action, applicants provisionally elect, without traverse, to prosecute the subject matter of Group II, i.e., Claims 11-17, and reserve the right to file one or more divisional applications directed to the non-elected subject matter in this application, i.e., Claims 1-10.

Wherefore, consideration and allowance of the claims of the present application are respectfully requested.

Respectfully submitted,


Leslie S. Szivos, Ph.D.
Registration No. 39,394

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